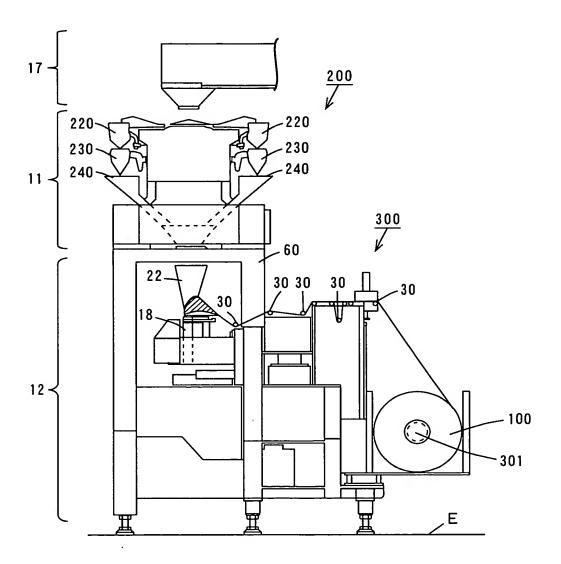
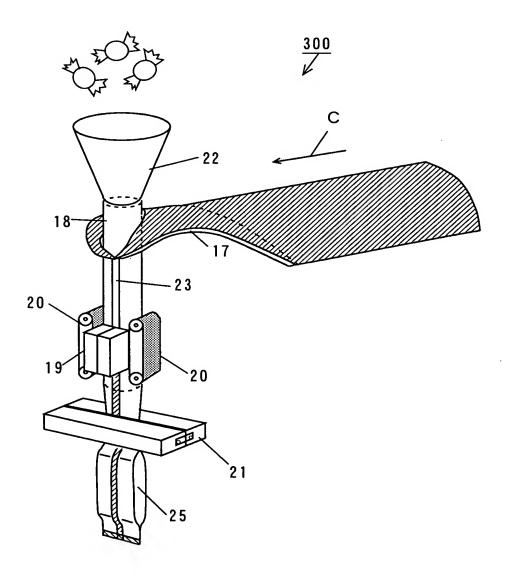
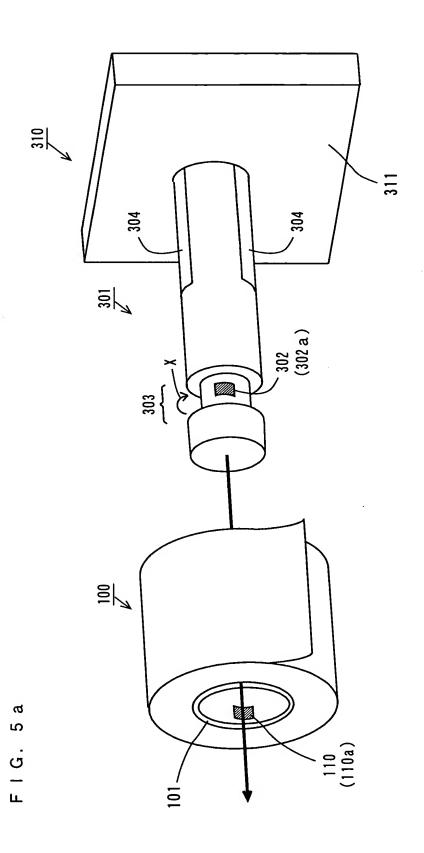


F I G. 3



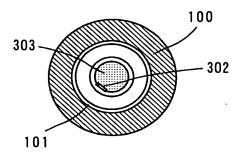
F I G. 4



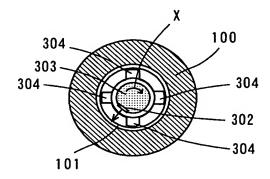


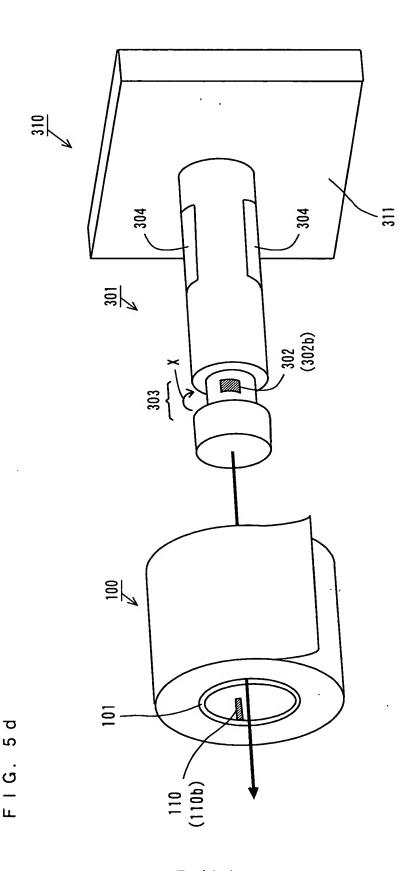
10/511051

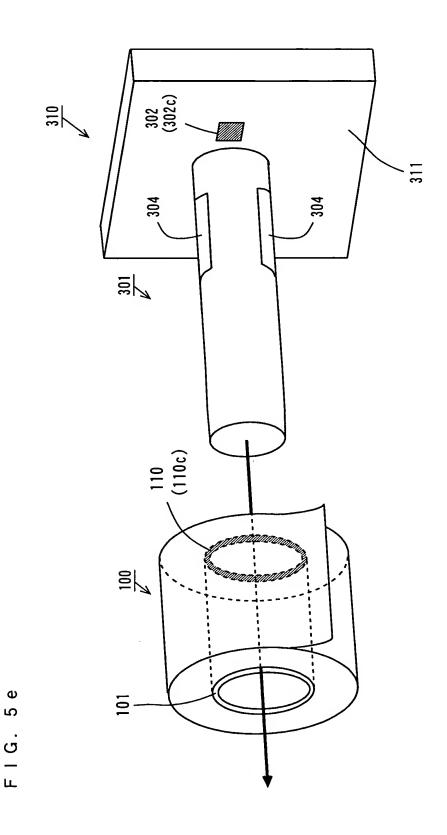
F I G. 5 b

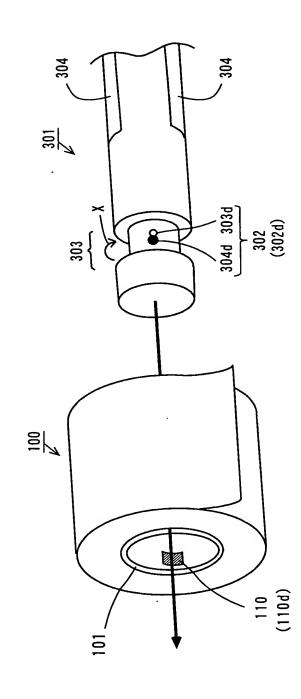


F I G. 5 c

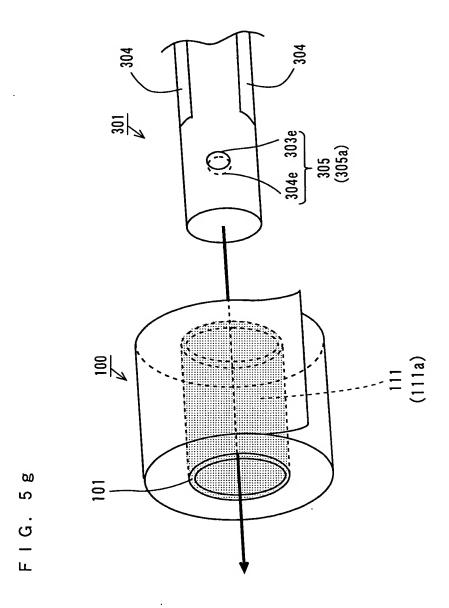








S



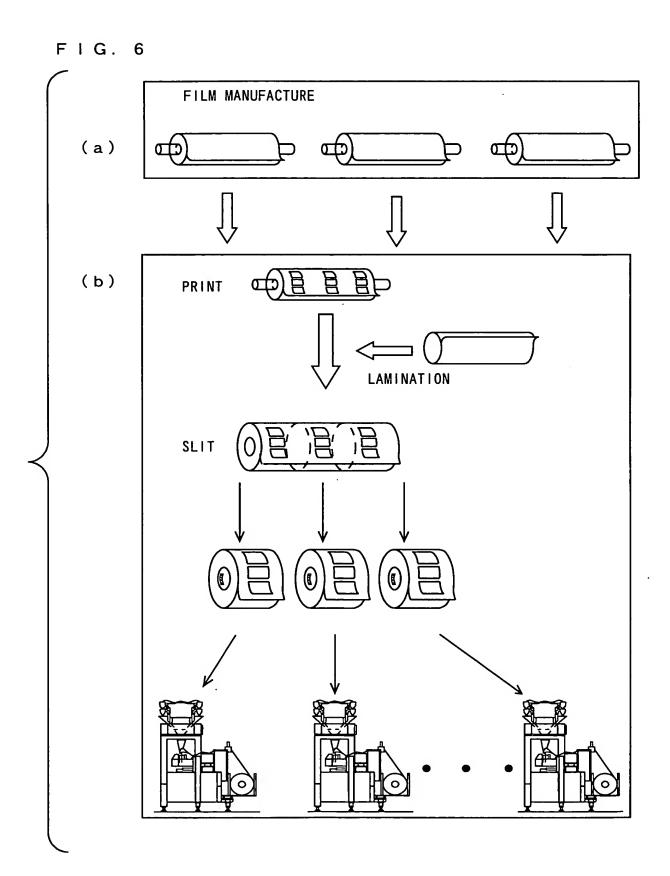
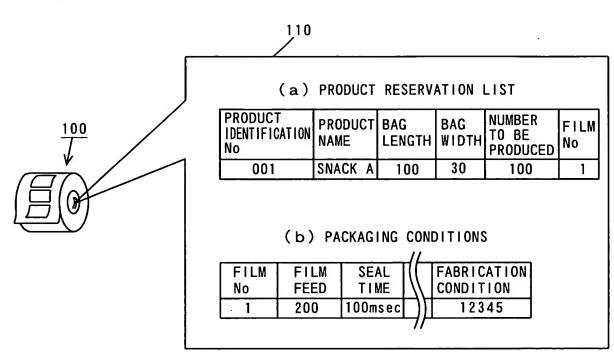


FIG. 7 500 501 5 0 2 INPUT/OUTPUT CPU ROMDEVICE **EXTERNAL** RECORDING RAM**STORAGE** MEDIUM READING DEVICE DEVICE 503 3 0 2 506 1 1 0

F I G. 8



PACKAGING MACHINE INVENTORS: Yukio NAKAGAWA, et al. SERIAL NO.: DOCKET NO.: 1745.1002

F I G. 9

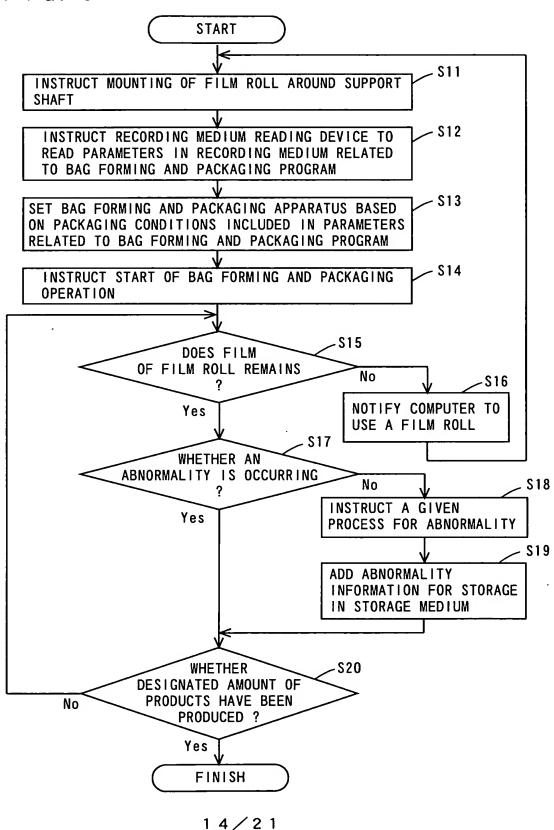
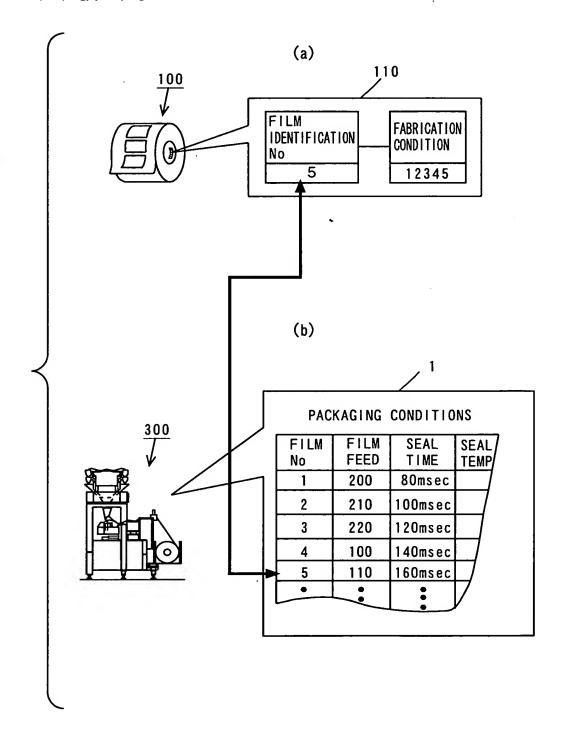
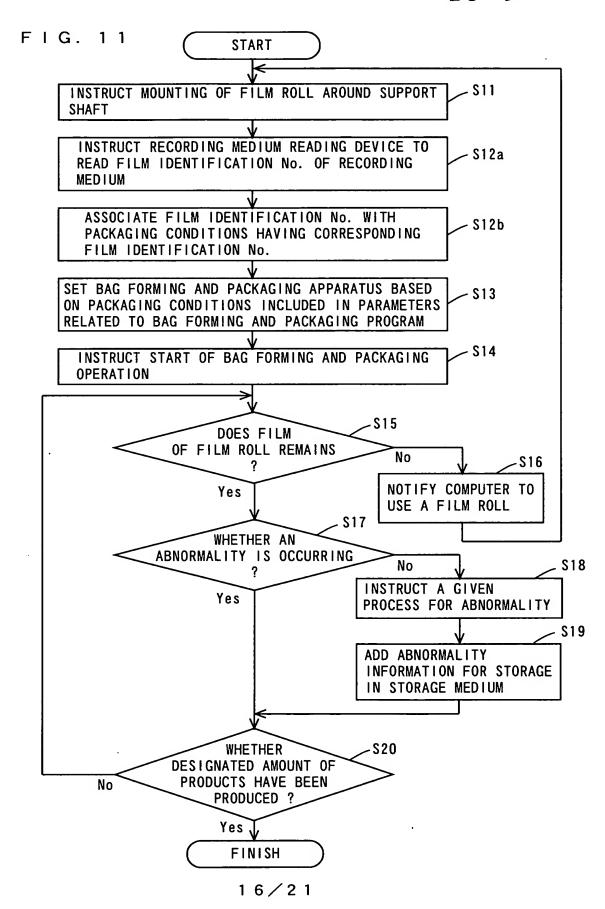


FIG. 10

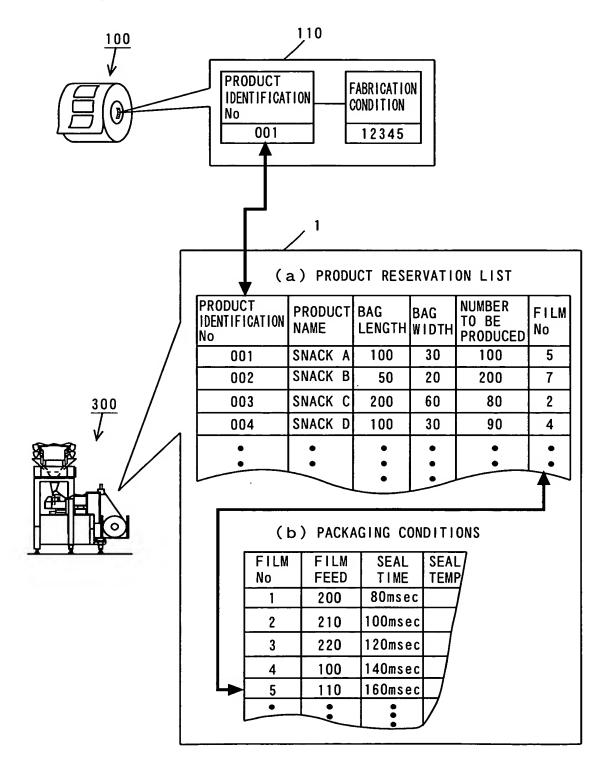


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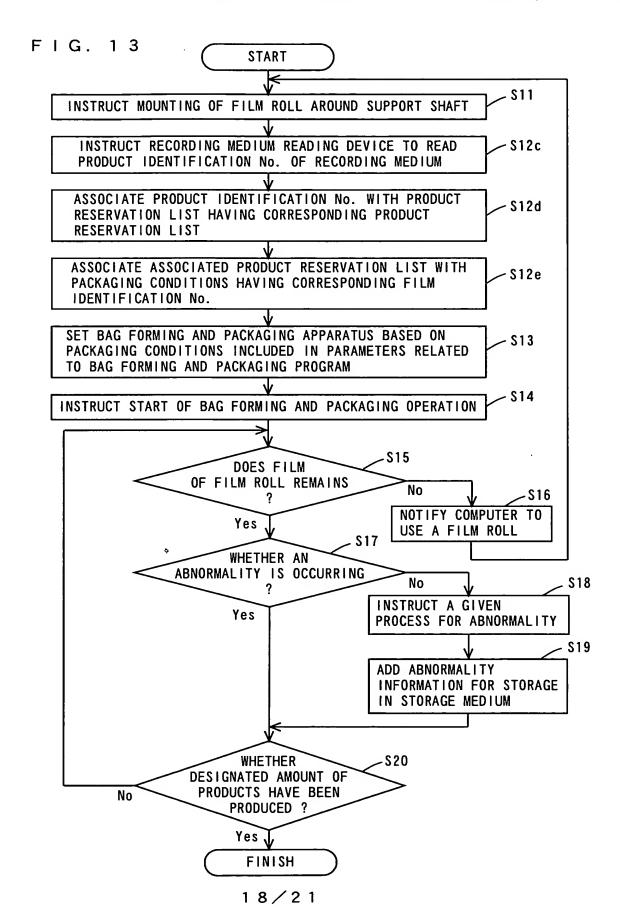


SERIAL NO.: 1745.1002

FIG. 12



PACKAGING MACHINE
INVENTORS: Yukio NAKAGAWA, et al.
SERIAL NO.:
DOCKET NO.: 1745.1002

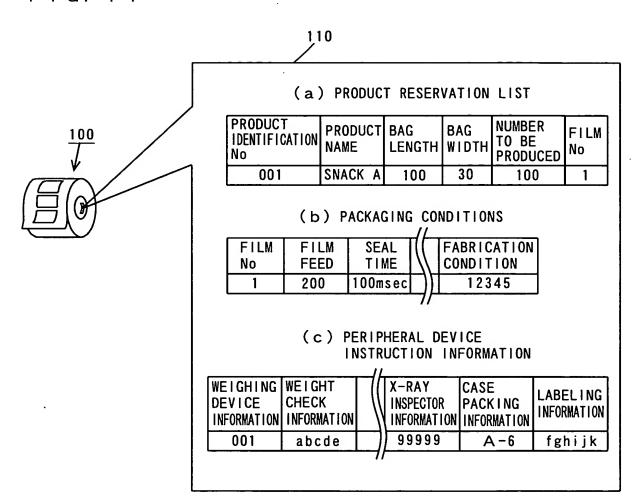


SERIAL NO.: 1745.1002

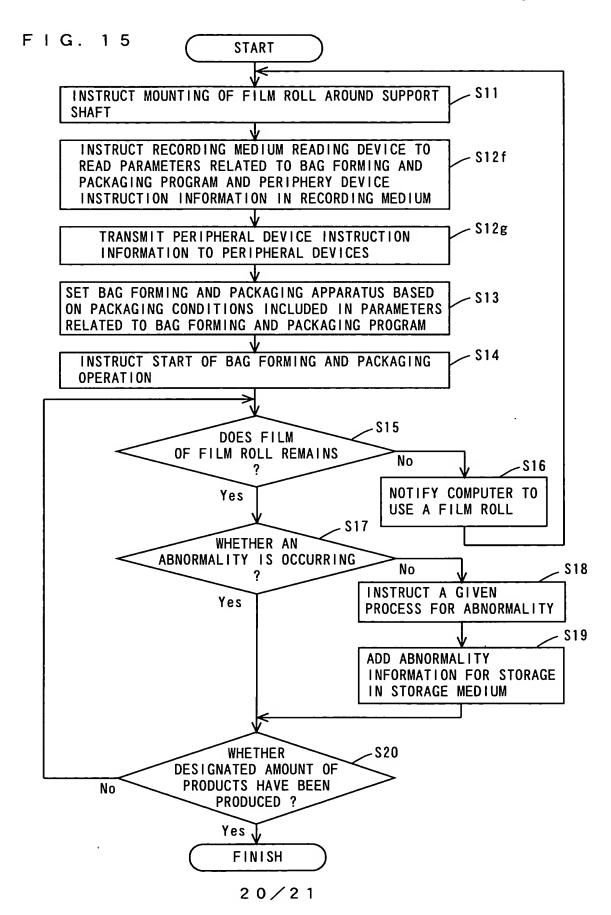
DOCKET NO .:

10/511051

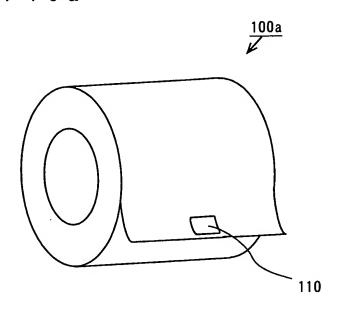
FIG. 14



PACKAGING MACHINE
INVENTORS: Yukio NAKAGAWA, et al.
SERIAL NO.:
DOCKET NO.: 1745.1002



F | G. 16a



F I G. 16b

